

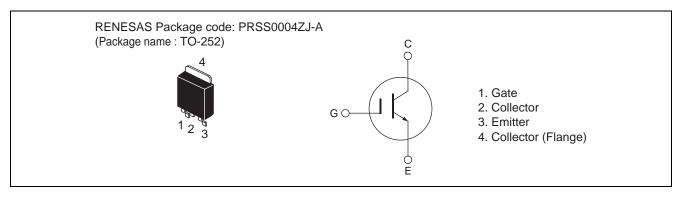
RJP30H1DPD

Silicon N Channel IGBT High speed power switching R07DS0465EJ0200 Rev.2.00 Jun 15, 2011

Features

- Trench gate and thin wafer technology (G6H-II series)
- High speed switching: $t_r = 80$ ns typ., $t_f = 150$ ns typ.
- Low collector to emitter saturation voltage: $V_{CE(sat)} = 1.5 V$ typ.
- Low leak current: $I_{CES} = 1 \ \mu A \ max$.

Outline



Absolute Maximum Ratings

 $(Tc = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Collector to emitter voltage	V _{CES}	360	V
Gate to emitter voltage	V _{GES}	±30	V
Collector current	lc	30	А
Collector peak current	ic(peak) Note1	200	А
Collector dissipation	Pc ^{Note2}	40	W
Junction to case thermal impedance	өј-с	3.13	°C/W
Junction temperature	Tj	150	C
Storage temperature	Tstg	-55 to +150	C

Notes: 1. PW \leq 10 $\mu s,\,duty\,cycle \leq$ 1%

2. Tc = 25°C

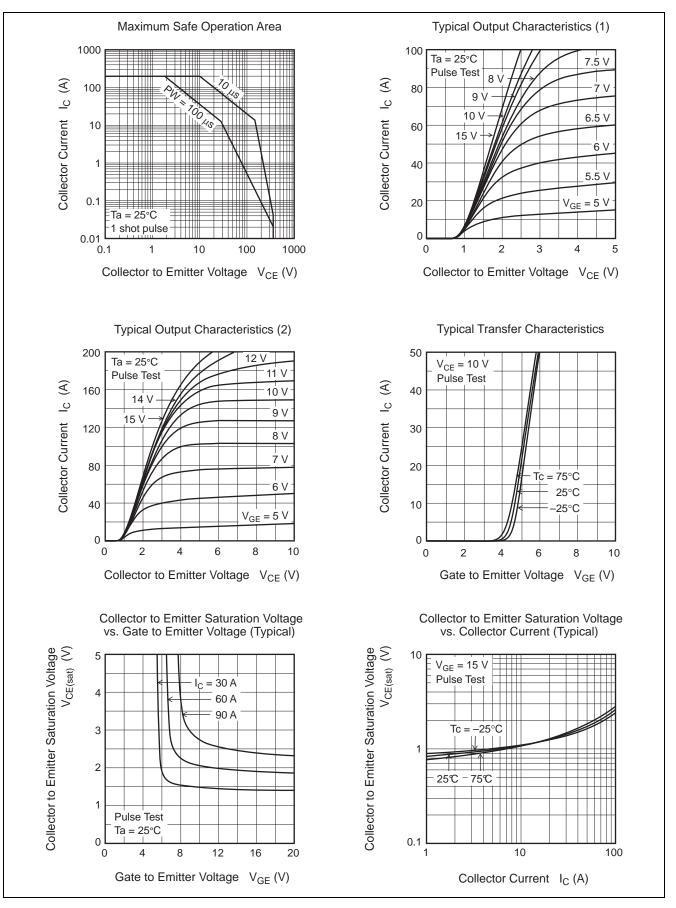
Electrical Characteristics

						$(Tj = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero gate voltage collector current	I _{CES}	_	—	1	μΑ	$V_{CE} = 360 \text{ V}, \text{ V}_{GE} = 0$
Gate to emitter leak current	I _{GES}		—	±100	nA	$V_{GE} = \pm 30 \text{ V}, V_{CE} = 0$
Gate to emitter cutoff voltage	V _{GE(off)}	2.5	—	5	V	$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ mA}$
Collector to emitter saturation voltage	V _{CE(sat)}	_	1.5	2	V	$I_{C} = 30A, V_{GE} = 15 V^{Note3}$
Input capacitance	Cies	_	740	_	pF	$V_{CE} = 25 V$ $V_{GE} = 0$ $f = 1 MHz$
Output capacitance	Coes	_	40	_	pF	
Reveres transfer capacitance	Cres	_	17	_	pF	
Total gate charge	Qg	_	23	_	nC	V _{GE} = 15 V V _{CE} = 150 V I _C = 30 A
Gate to emitter charge	Qge	_	4	_	nC	
Gate to collector charge	Qgc		8	—	nC	
Switching time	t _{d(on)}		0.02	—	μs	I _C = 30 A
	tr	_	0.08		μs	$R_L = 5 \Omega$
	t _{d(off)}	_	0.04		μs	$V_{GE} = 15 V$
	t _f	_	0.15	_	μs	$R_G = 5 \Omega$

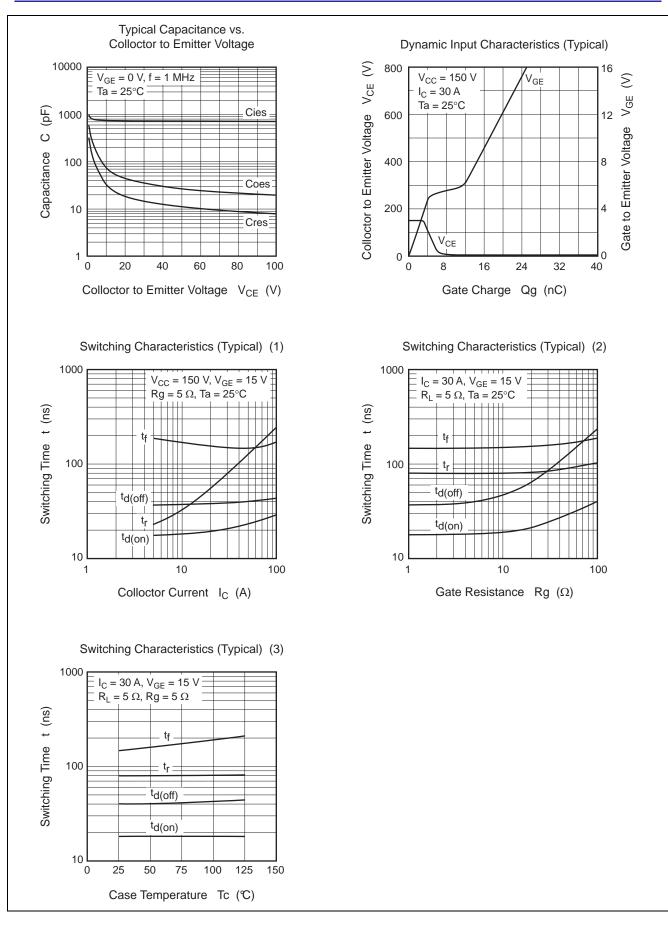
Notes: 3. Pulse test



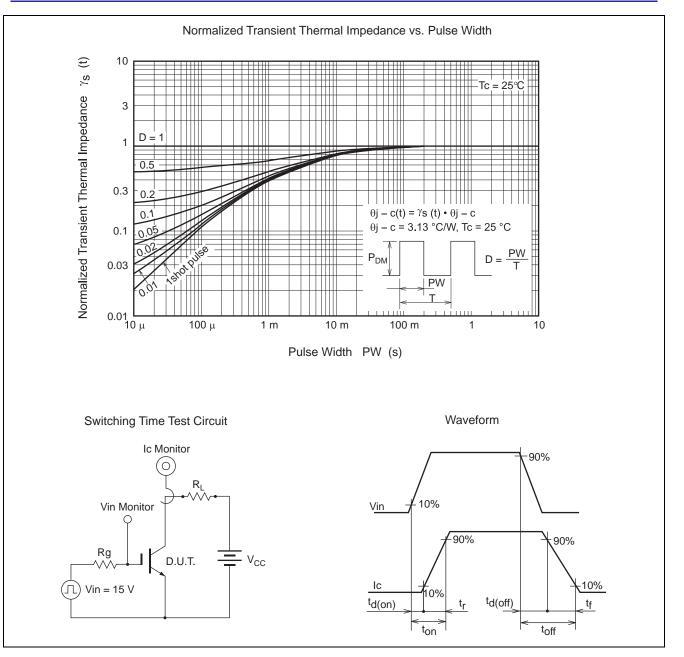
Main Characteristics





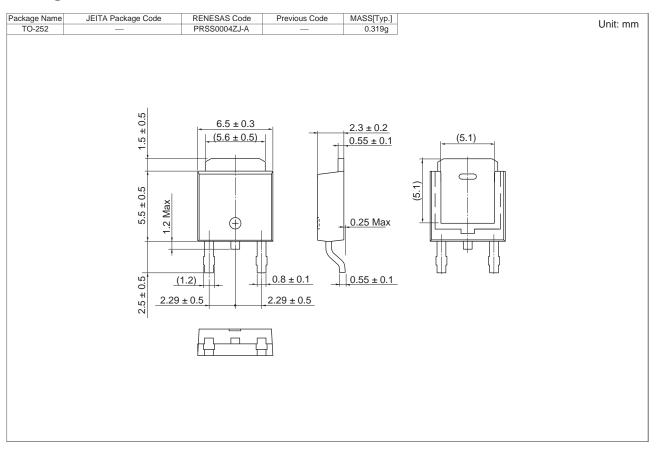








Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJP30H1DPD-00-J2	3000 pcs	Taping



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